FFR 0 2 2006 W

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Parte Patent Application of.	
Hisashi OHTANI et al.	
Serial No. 09/908,727	

Filed: July 20, 2001

For: SEMICONDUCTOR DEVICE AND

METHOD OF MANUFACTURING

THE SAME

Examiner: Richard A. Booth

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2006

RESPONSE

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The Official Action mailed September 28, 2005, has been received and its contents carefully noted. Filed concurrently herewith is a *Request for One Month Extension of Time*, which extends the shortened statutory period for response to January 30, 2006. Accordingly, the Applicants respectfully submit that this response is being timely filed.

The Applicants note with appreciation the consideration of the Information Disclosure Statements filed on July 20, 2001; August 27, 2001; February 19, 2002; September 20, 2002; April 17, 2003; June 2, 2004; October 14, 2004; October 20, 2004; and July 5, 2005.

Claims 1-56 are pending in the present application, of which claims 1-24 are independent. For the reasons set forth in detail below, all claims are believed to be in condition for allowance. Favorable reconsideration is requested.

The Official Action rejects claims 1-18 and 25-50 as obvious based on JP 07-038113 to Morosawa. The Applicants respectfully traverse the rejection because the Official Action has not made a *prima facie* case of obviousness.